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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER

A-411

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR

09/980959

INTERNATIONAL APPLICATION NO.  
PCT/JP99/02566

INTERNATIONAL FILING DATE  
18 May 1999

PRIORITY DATE CLAIMED  
18 May 1999

TITLE OF INVENTION

INDUCTOR ELEMENT

APPLICANT(S) FOR DO/EO/US

Akira OKAMOTO et al

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (24) indicated below.
4. ☒ The US has been elected by the expiration of 19 months from the priority date (Article 31).
5. ☒ A copy of the International Application as filed (35 U.S.C. 371 (c) (2))
  - a. ☐ is attached hereto (required only if not communicated by the International Bureau).
  - b. ☒ has been communicated by the International Bureau.
  - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☒ An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)).
  - a. ☒ is attached hereto.
  - b. ☐ has been previously submitted under 35 U.S.C. 154(d)(4).
7. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3))
  - a. ☐ are attached hereto (required only if not communicated by the International Bureau).
  - b. ☐ have been communicated by the International Bureau.
  - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
  - d. ☒ have not been made and will not be made.
8. ☐ An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☒ An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)).
10. ☒ An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).
11. ☒ A copy of the International Preliminary Examination Report (PCT/IPEA/409).
12. ☒ A copy of the International Search Report (PCT/ISA/210).

Items 13 to 20 below concern document(s) or information included:

13. ☐ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
14. ☒ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
15. ☒ A **FIRST** preliminary amendment.
16. ☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
17. ☐ A substitute specification.
18. ☐ A change of power of attorney and/or address letter.
19. ☐ A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825.
20. ☒ A second copy of the published international application under 35 U.S.C. 154(d)(4).
21. ☒ A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).
22. ☒ Certificate of Mailing by Express Mail
23. ☒ Other items or information:

**Inventor Information Sheet**

Copy of Form PCT/IB/308; Copy of Form PCT/IB/306

English translation of International Preliminary Examination Report

English translation of International Search Report ((Form PCT/ISA/210 (second sheet))

Express Mail #ZL89800333705

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 1.137(a)) <b>097/980959</b>	INTERNATIONAL APPLICATION NO. <b>PCT/JP99/02566</b>	ATTORNEY'S DOCKET NUMBER <b>A-411</b>
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24. The following fees are submitted: <b>BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) :</b>				<b>CALCULATIONS</b> PTO USE ONLY
<input type="checkbox"/> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO ..... <b>\$1040.00</b>				
<input checked="" type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO ..... <b>\$890.00</b>				
<input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO ..... <b>\$740.00</b>				
<input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) ..... <b>\$710.00</b>				
<input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) ..... <b>\$100.00</b>				
<b>ENTER APPROPRIATE BASIC FEE AMOUNT =</b>				<b>\$890.00</b>
Surcharge of <b>\$130.00</b> for furnishing the oath or declaration later than months from the earliest claimed priority date (37 CFR 1.492 (e)). <input type="checkbox"/> 20 <input type="checkbox"/> 30				<b>\$0.00</b>
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE	
Total claims	10 - 20 =	0	x \$18.00	\$0.00
Independent claims	1 - 3 =	0	x \$84.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>				<b>\$0.00</b>
<b>TOTAL OF ABOVE CALCULATIONS =</b>				<b>\$890.00</b>
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27). The fees indicated above are reduced by 1/2.				<b>\$0.00</b>
<b>SUBTOTAL =</b>				<b>\$890.00</b>
Processing fee of <b>\$130.00</b> for furnishing the English translation later than months from the earliest claimed priority date (37 CFR 1.492 (f)). <input type="checkbox"/> 20 <input type="checkbox"/> 30 +				<b>\$0.00</b>
<b>TOTAL NATIONAL FEE =</b>				<b>\$890.00</b>
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable). <input checked="" type="checkbox"/>				<b>\$40.00</b>
<b>TOTAL FEES ENCLOSED =</b>				<b>\$930.00</b>
				Amount to be: refunded \$ charged \$

- a. ☐ A check in the amount of \_\_\_\_\_ to cover the above fees is enclosed.
- b. ☐ Please charge my Deposit Account No. \_\_\_\_\_ in the amount of \_\_\_\_\_ to cover the above fees. A duplicate copy of this sheet is enclosed.
- c. ☐ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. \_\_\_\_\_. A duplicate copy of this sheet is enclosed.
- d. ☒ Fees are to be charged to a credit card. **WARNING:** Information on this form may become public. **Credit card information should not be included on this form.** Provide credit card information and authorization on PTO-2038.

**NOTE:** Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

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OCT 29, 2001  
DATE

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Akira OKAMOTO et al

S. N.

International S.N.: PCT/JP99/02566

Filed:

International Filing Date: 18 May 1999

For: INDUCTOR ELEMENT

PRELIMINARY AMENDMENT

Box PCT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Please make the following amendments to this application prior to examination thereof:

In the Specification:

Page 6, line 30 through page 7, line 6, please replace this paragraph with the following paragraph:

-- In an inductor element which consists of one layer of electrode with a spiral shape, in a state that the inductor element is sufficiently separated from other conductive members, as shown in FIG. 4, as the frequency of an input signal becomes high, the forward gain becomes smaller. This is because the fact that the impedance of the inductor element having the predetermined inductance  $L$  is  $j\omega L$  and becomes large in proportion to the frequency of the input signal. --

In the Drawings:

On sheet 4/8 of the drawings, please amend FIG. 6 as indicated in red on the drawing sheet attached herewith.

REMARKS

The amendment to the specification is to correct a printing font error wherein the symbol  $j\omega L$  did not print correctly. This change is apparent from the subject matter, and is apparent from

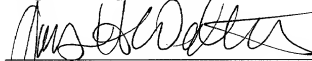
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review of the international publication page 4, line 27. A markup sheet is enclosed illustrating the change as is a replacement page 7. The above amendment to the drawings is presented to correct a typographical error and the correction is apparent from review of the specification, since, for example, in the specification at page 8 line 8, it is noted that the resonance point appears at nearly 84 MHz.

The amendment to drawings is incorporated in a replacement sheet 4/8 of the drawings that is filed herewith.

Respectfully submitted,



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element is sufficiently separated from other conductive members, as shown in FIG. 4, as the frequency of an input signal becomes high, the forward gain becomes smaller. This is because the fact that the impedance of the inductor element having the predetermined inductance  $L$  is  $j\omega L$  and becomes large in proportion to the frequency of the input signal.

On the other hand, as shown in FIG. 5, in the state that this inductor element is closely contacted to the copper plate, it is possible to maintain a high forward gain even if the input signal changes. This shows that this inductor element does not function as an original inductor since the inductance that this inductor element has becomes small by bringing the copper plate close to this inductor element. An estimated cause of decreasing the inductance is that eddy currents arise on the front side of the copper plate owing to magnetic flux, which is generated when a signal is inputted into the electrode, and cancel this magnetic flux.

In addition, in the measurement described above, although the copper plate was used as a substrate, even in the case that a semiconductor substrate is used instead of the copper plate, the same phenomenon happens fundamentally.

FIG. 6 is a graph showing the measurement result of the forward gain of an inductor element having two layers of electrodes that have the same shape and arrangement as the two conductors 1 and 2 included in the inductor element 10. In addition, FIG. 7 shows the forward gain of an inductor element having two layers of electrodes that have the same shape and arrangement as the two conductors 1 and 2 included in the inductor element 10, at the time of closely contacting a conductor substrate to this inductor element.

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FIG. 6

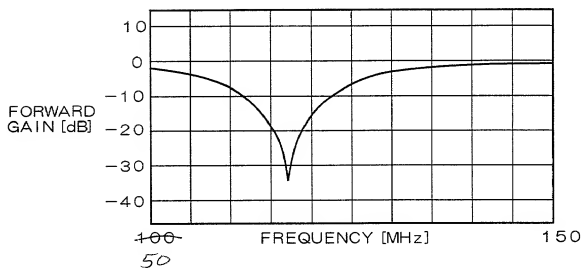
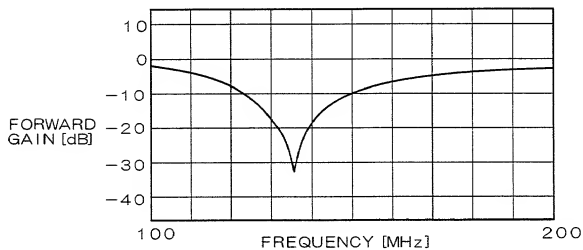


FIG. 7



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element is sufficiently separated from other conductive members, as shown in FIG. 4, as the frequency of an input signal becomes high, the forward gain becomes smaller. This is because the fact that the impedance of the inductor element having the predetermined inductance  $L$  is  $j\omega L$  and becomes large in proportion to the frequency of the input signal.

On the other hand, as shown in FIG. 5, in the state that this inductor element is closely contacted to the copper plate, it is possible to maintain a high forward gain even if the input signal changes. This shows that this inductor element does not function as an original inductor since the inductance that this inductor element has becomes small by bringing the copper plate close to this inductor element. An estimated cause of decreasing the inductance is that eddy currents arise on the front side of the copper plate owing to magnetic flux, which is generated when a signal is inputted into the electrode, and cancel this magnetic flux.

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FIG. 6

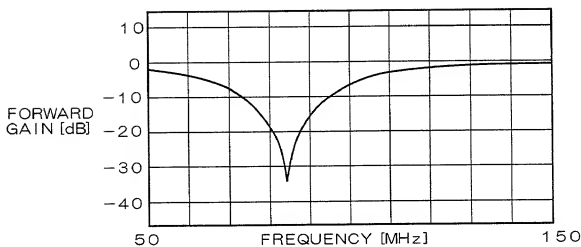
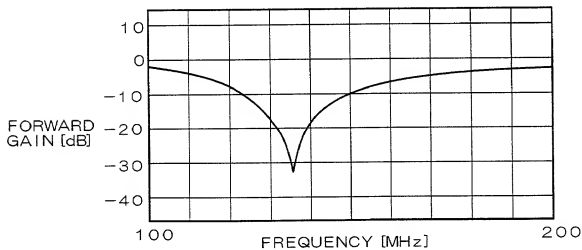


FIG. 7





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## SPECIFICATION

INDUCTOR ELEMENT

## TECHNICAL FIELD

The present invention relates to an inductor element that can be formed on various kinds of substrates such as a semiconductor substrate.

## BACKGROUND ART

There is known such a semiconductor circuit that forms a spiral pattern on a semiconductor substrate by using thin film forming technique, and uses this pattern as an inductor element. When current flows in such an inductor element that is formed on a semiconductor substrate, magnetic flux is generated in the direction perpendicular to a spiral pattern. Nevertheless, since eddy currents are induced on the front side of the semiconductor substrate by this magnetic flux to cancel effective magnetic flux, there is such a problem that doesn't effectively function as the inductor element. In particular, since the higher a frequency of a signal which flows in the inductor element becomes, the more remarkable this inclination becomes, and consequently it is difficult to form a high frequency circuit including the inductor element on a semiconductor substrate.

## DISCLOSURE OF THE INVENTION

The present invention is achieved in view of such a point. Its object is to provide an inductor element that effectively functions even if the inductor element is formed on a substrate.

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An inductor element according to the present invention is formed by superposing two conductors one on the other on a substrate with the two conductors sandwiching an insulating layer, one end of one of the two conductors is connected to one end of the other conductor, and the upper conductor is used as an inductor conductor. It is experimentally confirmed that an inductor element that has such structure has predetermined inductance without losing any inductance component by eddy currents etc. even if the inductor element is formed on a substrate, and hence it effectively functions as an inductor element.

In particular, it is preferable to form two conductors as described above, in the long shapes having substantially the same shape. Since an upper conductor never directly faces the front side of the substrate owing to making the two conductors the same in shapes, it is possible to reduce the eddy currents that are generated on the substrate when the upper conductor directly faces the substrate. In addition, it is possible to give predetermined inductance to the upper conductor by making the shapes of the two conductors be long. In particular, since it is possible to give large inductance to a conductor when the conductor is formed in one or more turns of spiral shape or a meander shape, the conductor is suitable for being built in a comparatively low frequency circuit. In addition, since it is possible to give a small inductance to a conductor when the conductor is formed in a circular shape less than one turn or an substantially linear shape, the conductor is suitable for being built in a comparatively high frequency circuit.

Moreover, when two conductors are made in spiral shapes, it is preferable to connect an inner end of one conductor with

an outer end of another conductor. Since it is experimentally confirmed that it is possible to secure further large inductance when an inductor conductor is formed on a substrate owing to performing such connection, it is possible to realize the inductor element that effectively functions on a substrate.

In addition, the inductor element described above is suitable for use as a compound element that also has a capacitance component besides an inductance component. Since this inductor element has two conductors that are superimposed one on the other and its characteristics also include a capacitance component, this inductor element can be used in applications where an inductor and a capacitor are combined for making them a part of a circuit.

In addition, it is preferable that the inductor element described above is formed on a semiconductor substrate. In particular, if the inductor element that effectively functions can be formed on a semiconductor substrate, it is possible to form various kinds of fundamental parts, including the inductor element, on the semiconductor substrate. Hence it becomes possible to form all of the various circuits on the semiconductor substrate without using any external parts.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a schematic diagram showing a planar structure of an inductor element according to this embodiment;

FIG. 2 is a schematic diagram showing a connection state of two conductors shown in FIG. 1;

FIG. 3 is an expanded sectional view taken on a line I-I in FIG. 1;

FIG. 4 is a graph showing experimental result;

FIG. 5 is a graph showing experimental result;

FIG. 6 is a graph showing experimental result;

FIG. 7 is a graph showing experimental result;

FIG. 8 is a circuit diagram of an oscillation circuit including an inductor element;

FIG. 9 is a chart showing an output characteristic of the oscillation circuit shown in FIG. 8;

FIG. 10 is a chart showing an output characteristic of the oscillation circuit shown in FIG. 8; and

FIGs. 11A through 11C are schematic diagrams showing modified examples of the conductors included in inductor elements.

#### BEST MODE FOR CARRYING OUT THE INVENTION

Hereafter, an inductor element according to an embodiment where the present invention is applied will be specifically described with referring to drawings.

FIG. 1 is a schematic diagram showing a planar structure of an inductor element according to this embodiment that is formed on a substrate. An inductor element 10 according to this embodiment has two conductors 1 and 2 that have spiral shapes and are formed in the front side of a semiconductor substrate 3.

These two conductors 1 and 2 have substantially the same shape. As shown in FIG. 1, when viewed from the front side of the semiconductor substrate 3, they are formed so that the conductor 1, which becomes an upper layer, and another conductor 2, which becomes a lower layer, are formed by being superimposed one on the other substantially exactly. Each of the conductors 1 and 2 is formed of a thin film of metal such as aluminum or gold, or semiconductor material such as poly silicon.

FIG. 2 is a schematic diagram showing a connection state of the two conductors described above. As shown in FIG. 2, lead wires 6a and 6b are connected to an outer end (outer circumferential end) and an inner end (inner circumferential end) of the upper conductor 1 respectively, and the inner end of the upper conductor 1 and the outer end of the lower conductor 2 are connected with a connection wire 6c.

The upper conductor 1 functions as an inductor conductor, and is connected to a circuit (not shown) formed on the semiconductor substrate 3 through the lead wires 6a and 6b connected to ends thereof.

FIG. 3 is an expanded sectional view taken on a line I-I in FIG. 1. As shown in FIG. 2, an insulating layer 4 is formed on the front side of the semiconductor substrate 3, and the conductor 2 with a spiral shape is formed in a part of a top face of the insulating layer 4. In addition, an insulating layer 5 is formed on a top face of the insulating layer 4 and conductor 2, and the conductor 1 is formed on a top face thereof.

The inductor element 10 according to this embodiment has the structure described above. Since predetermined inductance appears between two lead wires 6a and 6b connected to both ends of the upper conductor 1 respectively, this upper conductor 1 can be used as an inductor conductor. In addition, under this upper conductor 1, the conductor 2 that has substantially the same shape as this conductor 1 is formed. It is possible to suppress the generation of eddy currents in the front side of the semiconductor substrate 3 when an upper conductor 1 is used as an inductor conductor, by connecting one end of the upper conductor 1 with one end of the lower conductor 2 with the connection wire 6c. Hence it is possible to make the upper conductor 1 effectively function

as an inductor conductor. Therefore, it becomes possible to integrate the entire circuit, including the inductor element, in one piece on the semiconductor substrate 3.

Next, the result of a comparative experiments for estimating the characteristics of the inductor element 10 according to this embodiment described above will be described.

FIG. 4 is a graph showing the measurement result of the forward gain of an inductor element which has an electrode of one layer having the same shape as the conductor 1 included in the inductor element 10. In addition, FIG. 5 shows a characteristic, which is the forward gain of an inductor element which has an electrode of one layer having the same shape as the conductor 1 included in the inductor element 10, at the time of closely contacting a conductor substrate to this inductor element.

As for the inductor element used in this measurement, a five-turn electrode having one-mm pattern width and a 0.2-mm adjacent gap of spiral pattern was formed on a front side of a 0.13-mm-thick insulating member with a dielectric constant of 3.17. The characteristic shown in FIG. 4 was measured with sufficiently separating this inductor element from other conductive members. The characteristic shown in FIG. 5 was measured with closely contacting a copper plate to a front side of the insulating member in an opposite electrode side of this inductor element. In addition, vertical axes in FIGS. 4 and 5 (this is the same also in FIGS. 6 and 7 which are described later) denote attenuations logarithmically expressed, and horizontal axes denote the frequencies of input signals logarithmically expressed, respectively.

In an inductor element which consists of one layer of electrode with a spiral shape, in a state that the inductor

element is sufficiently separated from other conductive members, as shown in FIG. 4, as the frequency of an input signal becomes high, the forward gain becomes smaller. This is because the fact that the impedance of the inductor element having the predetermined inductance  $L$  is  $j\omega L$  and becomes large in proportion to the frequency of the input signal.

On the other hand, as shown in FIG. 5, in the state that this inductor element is closely contacted to the copper plate, it is possible to maintain a high forward gain even if the input signal changes. This shows that this inductor element does not function as an original inductor since the inductance that this inductor element has becomes small by bringing the copper plate close to this inductor element. An estimated cause of decreasing the inductance is that eddy currents arise on the front side of the copper plate owing to magnetic flux, which is generated when a signal is inputted into the electrode, and cancel this magnetic flux.

In addition, in the measurement described above, although the copper plate was used as a substrate, even in the case that a semiconductor substrate is used instead of the copper plate, the same phenomenon happens fundamentally.

FIG. 6 is a graph showing the measurement result of the forward gain of an inductor element having two layers of electrodes that have the same shape and arrangement as the two conductors 1 and 2 included in the inductor element 10. In addition, FIG. 7 shows the forward gain of an inductor element having two layers of electrodes that have the same shape and arrangement as the two conductors 1 and 2 included in the inductor element 10, at the time of closely contacting a conductor substrate to this inductor element.

The inductor element used for these measurements has the structure that an electrode corresponding to the conductor 2 shown in FIG. 1 is added to the inductor element shown in the measurement result in FIGS. 4 and 5. In addition, if closely contacting the copper plate to this inductor element, the lower electrode and copper plate are arranged through an adequate thin insulating member.

A resonance point appears at nearly 84 MHz as it is apparent from the forward gain characteristic shown in FIG. 6 in a state that the inductor element where two layers of electrodes which have spiral shapes is sufficiently separated from other conductive members. This is because this inductor element functions as a whole as a compound device that has an inductance component and a capacitance component since capacitance is produced between two electrodes while one electrode functions as an inductor conductor.

In addition, in a state of closely contacting this inductor element to the copper plate, as shown in FIG. 7, a similar resonance point appears although the position of a resonance point (135 MHz) shifts. This shows that, by using the inductor element that has the double structure of the electrodes described above, even if the inductor element is closely contacting to the copper plate, the inductance component does not disappear and the function as an inductor conductor is maintained.

In addition, since the capacitance between two electrodes surely accompanies the inductor element that has double-structure electrodes, it is preferable to use it as one part of a circuit, where its resonance characteristic is used, as an application. For example, it is preferable to



use it as an inductor element 10 in an oscillation circuit, a tuning circuit, or the like.

Next, a specific example at the time of using the inductor element 10 according to this embodiment described above as one part of an actual circuit will be described.

FIG. 8 shows the structure of a Clapp oscillator that is an example of an oscillation circuit constituted by using the inductor element 10 according to this embodiment. In this Clapp oscillator, the capacitance of two capacitors 20 and 22 may be several tens times the between-terminal capacity of a transistor 24. In addition, an inductor element 30 is connected through a capacitor 28.

FIG. 9 is a chart showing the output characteristic of an oscillation circuit where the inductor element, which has one layer of electrode and the measurements of whose forward gain is shown in FIG. 4, is used as the inductor element 30 in the Clapp oscillator shown in FIG. 8. As shown in FIG. 9, an oscillation frequency of 119 MHz was observed.

In addition, FIG. 10 is a chart showing the output characteristic of an oscillation circuit where the inductor element, whose two-layer electrodes are closely contacted to the copper plate and the measurements of whose forward gain is shown in FIG. 7, is used as the inductor element 30 in the Clapp oscillator shown in FIG. 8. As shown in FIG. 10, an oscillation frequency of 127 MHz is observed.

Thus, it can be seen that the inductor element having the two-layer electrodes functions as an inductor conductor without losing an inductance component even if it is closely contacted to the copper plate in its one side (the side opposite to the electrode used as the inductor conductor). Hence, the inductor element 10 according to this embodiment which has

the fundamentally same structure can effectively function as an inductor element which has predetermined inductance even if it is formed so that it may be closely contacted to the front side of the semiconductor substrate 3 by providing the lower conductor 2 besides the upper conductor 1.

In addition, the present invention is not limited to the above-described embodiment, but various types of modifications are possible within the scope of the gist of the present invention. For example, in the inductor element 10 shown in FIGs. 1 and 2, the inner end of the upper conductor 1 and the outer end of the lower conductor 2 are connected through the connection wire 6C. Nevertheless, on the contrary, it can be also performed to mutually connect the outer end of the upper conductor 1 and the inner end of the lower conductor 2. In addition, it can be also performed to connect either outer ends or both inner ends of conductors 1 and 2 when it is allowed that the inductance of an inductor element becomes small to some extent.

In addition, since two conductors 1 and 2 included in the inductor element 10 are formed in spiral shapes in the embodiment described above, it is possible to realize the inductor element 10 having large inductance. Nevertheless, it is also good to form two conductors 1 and 2 in meander shapes (FIG. 11A). In addition, the inductance of this inductor element 10 can be small when it is used as one part of a high frequency circuit. Hence it is also good to form the inductor element 10 less than one turn by reducing the numbers of turns of the conductors 1 and 2 (FIG. 11B), or to form it in an substantially linear shape (FIG. 11C).

In addition, although the shapes of two conductors 1 and 2 are set substantially similarly in the embodiment described

above, it is also good to set them in different shapes. For example, it can be also performed to set the number of turns of the lower conductor 2 to be more than that of the upper conductors 1. Thus, since the upper conductor 1 does not directly face the semiconductor substrate 3 if all or some of the lower conductor 2 is arranged under the upper conductor 1, it is possible to effectively prevent the generation of eddy currents due to the upper conductor.

In addition, although the inductor element 10 is formed by forming two conductors 1 and 2 on the semiconductor substrate 3 in the embodiment described above, the inductor element 10 in which two conductors 1 and 2 are formed on a conductor substrate such as a metal is also realizable. From the experimental result shown in FIG. 5, it is confirmed that, even in this case, this effectively functions as the inductor element 10. If it becomes possible to form the inductor element 10 by closely contacting it on a conductor substrate, it also becomes possible to arrange the inductor element 10 on a front side of a metal shielding case or the like. Hence it becomes easy to secure an installation space of the inductor element.

#### INDUSTRIAL APPLICABILITY

As described above, according to the present invention, by forming two conductors one on the other on a substrate and connecting one end of one conductor with one end of the other, it is possible to use an upper conductor as an inductor conductor having predetermined inductance. Hence it is possible to make it effectively function as the inductor element. In particular, by spirally forming two conductors and connecting the inner end of one conductor with the outer end of the other conductor, it becomes possible to secure large inductance.

In addition, according to the present invention, since an inductor element that effectively functions on a semiconductor substrate is realized, it becomes possible to integrate all the parts including an inductor, which is impossible up to now.

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CLAIMS

1. An inductor element, characterized in that said element comprises two conductors one on the other on a substrate in a state where the two conductors are mutually insulated, and one end of one conductor is connected with one end of the other while the conductor that is an upper layer is used as an inductor conductor.
2. The inductor element according to claim 1, characterized in that said two conductors have substantially the same shape.
3. The inductor element according to claim 1, characterized in that said two conductors have long shapes, and one end of one conductor in a longitudinal direction is connected with one end of the other in the longitudinal direction.
4. The inductor element according to claim 1, characterized in that said two conductors have circular shapes less than one turn, and one end of one conductor is connected with one end of the other.
5. The inductor element according to claim 1, characterized in that said two conductors have spiral shapes each number of turns of which is one or more, and one end of one conductor is connected with one end of the other.
6. The inductor element according to claim 1, characterized in that the two conductors are formed in substantially linear shapes, and one end of one conductor is connected with one end of the other.
7. The inductor element according to claim 1, characterized in that the two conductors are formed in meander shapes, and one end of one conductor is connected with one end of the other.

8. The inductor element according to claim 5, characterized in that an inner end of said one conductor is connected with an outer end of the other conductor.

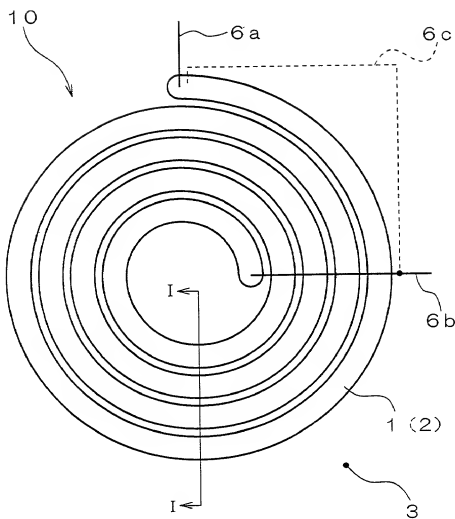
9. The inductor element according to claim 1, characterized by further comprising:

an inductance component of the conductor that is an upper layer; and

a capacitance component between the two conductors.

10. The inductor element according to claim 1, characterized in that said substrate is a semiconductor substrate.

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FIG. 1



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FIG. 2

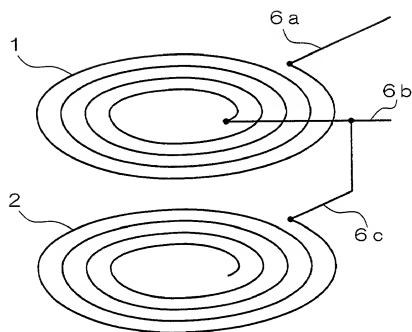
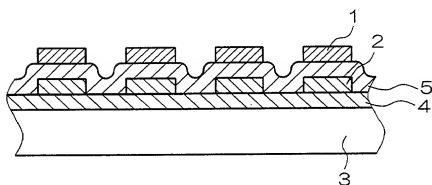


FIG. 3





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FIG. 4

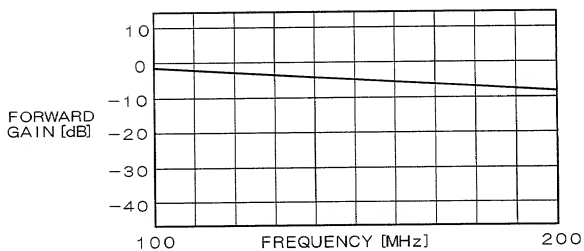
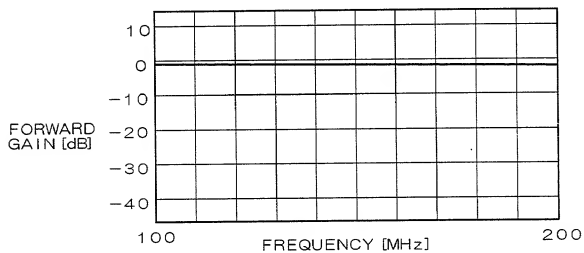


FIG. 5



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FIG. 6

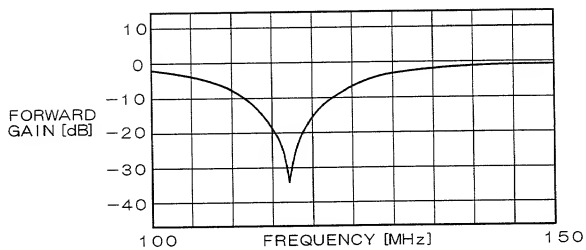
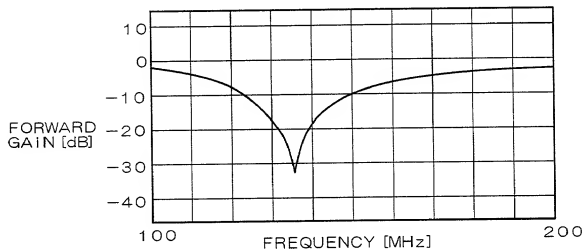
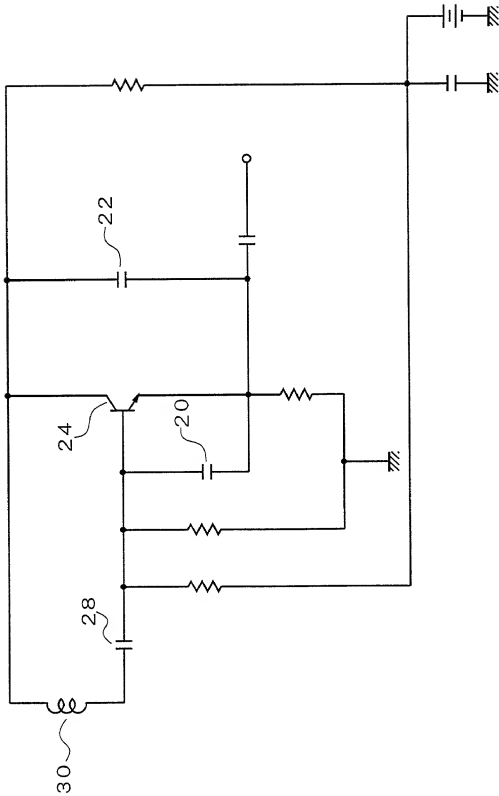


FIG. 7

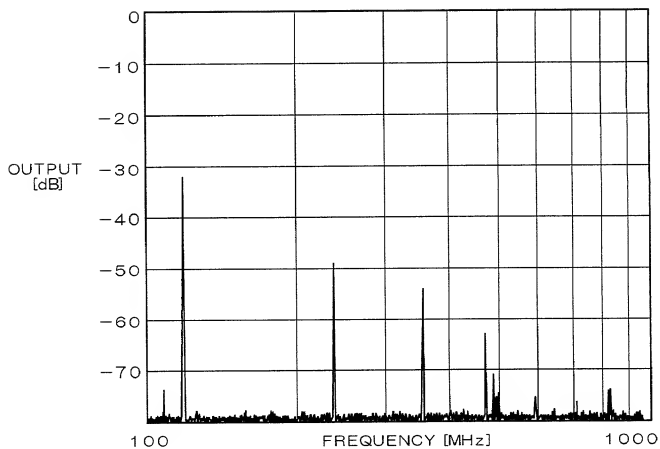


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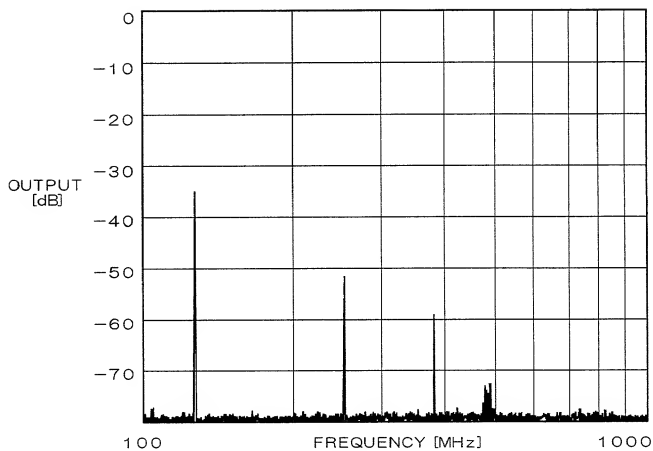
FIG. 8



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FIG. 9



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FIG. 10



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FIG. 11A

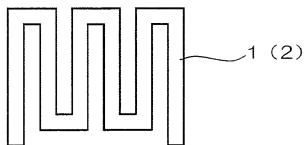


FIG. 11B

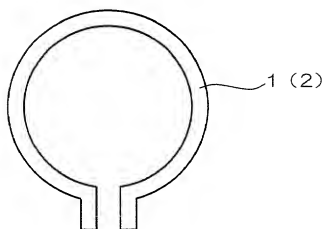
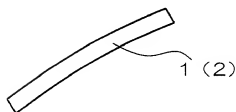


FIG. 11C



Docket No.

A-411

# Declaration and Power of Attorney For Patent Application

## English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

### INDUCTOR ELEMENT

the specification of which

(check one)

☐ is attached hereto.

☒ was filed on 18 May 1999 as United States Application No. or PCT International Application Number PCT/JP99/02566 and was amended on \_\_\_\_\_

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

(Number)

(Country)

(Day/Month/Year Filed)

☐

(Number)

(Country)

(Day/Month/Year Filed)

☐

(Number)

(Country)

(Day/Month/Year Filed)

☐

I hereby claim the benefit under 35 U.S.C. Section 119(e) of any United States provisional application(s) listed below:

\_\_\_\_\_  
(Application Serial No.)

\_\_\_\_\_  
(Filing Date)

\_\_\_\_\_  
(Application Serial No.)

\_\_\_\_\_  
(Filing Date)

\_\_\_\_\_  
(Application Serial No.)

\_\_\_\_\_  
(Filing Date)

I hereby claim the benefit under 35 U. S. C. Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C. F. R., Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

\_\_\_\_\_  
(Application Serial No.)

\_\_\_\_\_  
(Filing Date)

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(Status)  
(patented, pending, abandoned)

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(Application Serial No.)

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(Filing Date)

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(patented, pending, abandoned)

\_\_\_\_\_  
(Application Serial No.)

\_\_\_\_\_  
(Filing Date)

\_\_\_\_\_  
(Status)  
(patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.



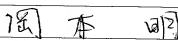
POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)


James H. Walters, Reg. No. 35,731

I authorize the attorney that I have appointed to accept instructions regarding this application and the resulting patent from Amagai Patent Firm.

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 Portland, Oregon 97204 US

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